

Title: Implementing Access Control Lists Using A Balanced
Hash Table of Access Control List Binary Comparison Trees

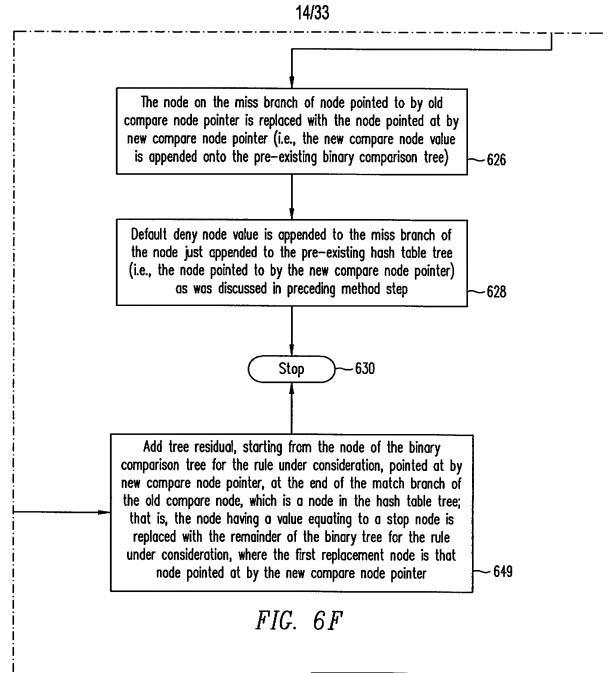


FIG. 6A	FIG. 6B	FIG. 6C
FIG. 6D	FIG. 6E	FIG. 6F

Key To FIG. 6

**Application No.:** 09/483,110

First Named Inventor: Faisal Haq
Title: Implementing Access Control Lists Using A Balanced
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15/33

Simplified Example of Ordered ACL Rule Set Typically Entered by a Network Administrator	ypically Entered by a Network Administrator
ACL Rules in an Ordered ACL Rule Set expressed as plain english statements	Examples of Coded Versions of ACL Rules Which Are Typically Utilized Within an ACL Rule Set
Permit TCP protocol packets from any source IP address going to host hoving an IP address of 28.16.31.10 and a port identifier equal to 28.	PERMIT TCP ANY HOST 28.16.31.10 EQ 28
Deny TCP protocol packets from any source IP address going to host having an IP address of 28.16.31.10 and a port identifier greater than 23.	DENY TCP ANY HOST 28.16.31.10 GT 23
Deny UDP protocol packets from any source IP address going to host having an IP address of 30.22.12.5 and a port identifier equal to 11.	DENY UDP ANY HOST 30.22.21.5 EQ 11
Permit UDP protocol packets from any source IP address going to host having an IP address of 30.22.12.X, where X indicates any number, or "don't care".	PERMIT UDP ANY HOST 30.22.21.X
Deny all packets having source IP address of 23.20.7.0 and any destination address (indicated by address X.X.X.X, where X indicates any number, or "don't care").	DENY TCP 23.20.7.0 X.X.X.X.
Permit TCP protocol packets from any source IP address gaing to host having an IP address of 28.16.32.10.	PERMIT TCP ANY HOST 28.16.31.10

í						16/33	)		
ist One ACL Rule in the ACL Rule Set, and the	Destination	Port	111.01010.10111	111.01010.11100	101.00101.01011	J1.XXXXX.XXXXX	XXXXX.XXXXX	11.01010.XXXXX	555555555 ———Read Bit Position 30123456789 ———Slot Numbers Vertically. For example, the first bit position is denoted 0 0 0 0 0 1, the second, 2, the third, 3, the fourth, 4, 1 5 the eleventh, 1, and the fifty-ninth, 9.
Having One Field for Each Packet Header Field Utilized by at Least One ACL Rule in the ACL Rule Set, and the ule in the ACL Rule Set Based on the Created Exemplar	Source Destination	Address	01001.XXXXX.XXXXX.XXXXX.11100.10000.11111.01010.10111	01001.XXXXX.XXXXX.XXXXX.XXXXX.11100.10000.11111.01010.11100	11111.XXXXX.XXXXX.XXXXX.XXXXX.11110.10110.10101.00101.01011	11111.XXXXX.XXXXX.XXXXX.XXXXX.11110.10110.10101.XXXXX.XXXXX	01001.10111.10101.00111.00000.XXXXX.XXXXX.XXXXX.XXXXX.XXXXX	01001.XXXXX.XXXXX.XXXXX.11100.10000.11111.01010.XXXXX	0000000001111111112222222233333333334444444444
ng Having On Rule in the	Protocol	<b>a</b>	01001.X)	01001.X)	11111.XX	11111.XX	01001.10	01001.XX	00000000 12345678
Example of the Creation of an Exemplar Bit String Having One Field for Each Packet Header Field Utilized to Subsequent Creation of Bit Strings for each ACL Rule in the ACL Rule Set Based on the Created Exemplar	Construct Exemplar Bit String Bosed On Packet	Header Fields Utilized by ACL Rule Set Rules	Bit String, based on exemplar, for ACL Rule 1 with string "01001" associated with TCP protocol for sake of example.	Bit String, based on exemplar, for ACL Rule 2 with string "01001" associated with TCP protocol for sake of example.	Bit String, based on exemplar, for ACL Rule 3 with string "1111" associated with UDP protocol for sake of example.	Bit String, based on exemplar, for ACL Rule 4 with string "1111" associated with UDP protocol for sake of example.	Bit String, based on exemplar, for ACL Rule 5 with string"01001" associated with TCP protocol for sake of example.	Bit String, based on exemplar, for ACL Rule 6 with string "01001" associated with TCP protocol for sake of example.	Note: "Bit Position" is illustrated for sake of clarity and ease of counting herein as taking account of the periods between 5 bit fields; however, those skilled in the art will recognize that ordinarily such periods are not counted as bit positions.

Title: Implementing Access Control Lists Using A Balanced Hash Table of Access Control List Binary Comparison Trees

							17/33										
40440.01000.01010.11000.11111.00035.05335.02020.41313.11111	00000.55555.55555.66555.66666.11146.16446.13131.63535.44444	26226.10111.10101.00111.00000.55520.50220.53535.03131.2222 00000.55555.5555.5555.5555.11111.1111.1	26226.65666.65656.55666.55555.66631.61331.64645.25353.55555	46446.66666.66666.66666.66666.6646.6646.64645.65555.55555		20226.55555.5555.5555.5555.11131.11331.13131.23333.44444			Number of Unspecified Pointers of Bit Selection Vector = 4 For sake of example, assume hash table index having a hit length of 4 is specified	מספוויר נוסטן למסור ווסרא וסחוואל בי על החלקנו כן דום טף כיוויכני	वंब विव विव विव विव विव विव विव विव विव वि	Note: The row columns 1, 3, 34, 39, 41, and 46 of the Larger Total Count row had the smallest entries	(i.e., are used to mainline of formal and under the production will form the conditions it, at, at, at, at, and and 46 of the Larger Total Count row are designated as potential candidate bits "P."	R RR	Note: The row columns 1, 3, and 4 of the Smaller Total Count, row, corresponding with the selected row	columns of the Larger lotal Count_row, had the smallest entries (i.e., the base 10 number 2), and thus the bit positions associated with row columns 1, 3, and 4 of the Smaller Total Count_row are redesignated as	
Example of the Greation of a Bit Selection Vector "O" Count in Each Bit Postion:	Total of 0 + x Counts:	"1" Count in Each Bit Postion: "X" Count in Each Bit Postion:	Total of "1" + "X" Counts:	Construct a "Larger Total Count" row having one row entry corresponding to each bit position in the strings which were	constructed from the ACL rules; fill each row entry with the larger of either the Total of '0' + 'X' Counts" or Total of '1' + 'X' Counts" far the bit position corresponding to that row entry.	Construct a "Smaller Total Count" row having one row entry	constructed from the ACL rules; fill each row entry with the smaller of either the Total of '0' + 'X' Counts" or Total of '1' +	'X' Counts" for the bit position corresponding to that row entry.	Set number of unspecified pointers of bit selection vector = specified hit length of back table index	Specified by religion of most twice mock. Clock the raw entries in the furner Total Count? Row relimins	having the smallest number entries; designate the bit positions	corresponding to the selected row columns as potential, "P,"	of the bit selection vector	Since there are more potential, "P," candidate columns than	number of unspecified pointers of bit selection vector, refine	the selection by examining the columns of the Smaller Total	

Title: Implementing Access Control Lists Using A Balanced
Hash Table of Access Control List Binary Comparison Trees

	18/33	<b>,</b>	
candidate bits "R".	Since the number or redesignated potential candidates, "R," is less than the number of unspecified pointers of bit selection vector, designate all redesignated, "R," candidates as actual, "K," bit selection vector positions in the respective fields from which the respective bit strings were constructed will thereafter be pointers of the bit selection vector.	Number of unspecified pointers of bit selection vector =  number of unspecified pointers of bit selection vector (i.e., 4) —  number of specified actual, "K," bit selection vector pointer indication columns, whose corresponding  bit positions in the respective fields from which the repective bit strings were constructed will  thereafter be pointed at by pointers of the bit selection vector specified in preceding step (i.e., 3)  thereafter be pointed at by pointers of the bit selection vector specified in preceding step (i.e., 3)	Since the number of unspecified pointers of bit selection vector is still non-zero, mark specified "K," bit selection and selection to the specified "K," bit selection columns, whose corresponding bit have already been designated as candidates "K," bit selection vector pointer indication columns, whose corresponding bit have already been designated as candidates in the respective fields from which the respective fields from which the respective bit strings were constructed will thereafter be pointed at by
Count Row, with such examined Smaller Total Count Row columns being those corresponding to the Larger Total Count Row columns designated as potential, "P," candidate columns; redesignate as potential, "R," candidate columns which might be utilized as the pointers of the bit selection vector, those examined Smaller Total Count Row columns with the smallest number entries	Since the number or redesignated potential candidates, "R," is less than the number of unspecified pointers of bit selection vector, designate all redesignated, "R," candidates as actual, "K," bit selection vector Pointer Indication Columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector	Substract the number of specified actual, "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector, from number of unspecified pointers of bit selection vector	Since the number of unspecified pointers of bit selection vector is still non-zero, mark specified "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by

**Application No.:** 09/483,110

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Title: Implementing Access Control Lists Using A Balanced
Hash Table of Access Control List Binary Comparison Trees

1	9/33
	* ** Note: Row columns 1, 3, and 4 are marked with asterisks to indicate that since the bit positions associated with these row columns have already been designated as candidates.
pointers of the bit selection vector with asterisks indicating that such columns are no longer selectable or under consideration, since the bit positions associated with the "K," bit selection vector pointer indication calumns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector have already been specified	Thereafter, repeat the "select the row entries in the Larger Total Count" Row having smallest number entries " operation above upon the row columns which have not yet been designated as candidate "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector

Since there are more candidates, "P," than number of
unspecified pointers of bit selection vector (at this point,
3 pointers have been specified as "K," meaning that one
additional pointer is necessary to have the pointers
required to completely point out the 4 bit hash table
index), repeat the refine the selection operation above

## R RR RR

Note: Since all entries in the "Smaller Total Count" Row columns, corresponding with the selected row columns of the "Larger Total Count" Row, were the same number (i.e., the base ten number "3"), all P row columns are redesignated as condidates R".

Since after redesignation there are still more candidates "R" than the number of unspecified pointers of bit selection vector, all "R," candidates are deemed equally good choices; consequentially, the number of actual, "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector necessary to completely point out the hash table index value (i.e., in the present example, one more pointer is needed) may be selected at random from the designated "R" row columns.

K

Note: Select row column 34 at random.

There are now specified actual, "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector equal in number to the bit length of the hash table index; consequently, all pointers of the bit selection vector, which will be utilized to point to bit positions used to form a hash table index value which will be used to "key into"

l K KK

Note: These actual, "K," bit selection vector pointer indication columns, whose corresponding bit positions in the respective fields from which the respective bit strings were constructed will thereafter be pointed at by pointers of the bit selection vector indicate that the first, third, and fourth leftmost bit positions within the "protocol ID" field, and the fourth leftmost bit positions within the "destination address" field will be utilized as the hash table index bits.

we nash table, nave been lully specified.	
Definition of the bit selection vector  (pointer to first leftmost pointer to third leftmost pointer to fourth leftmost pointer t	Bit Selection Vector =  (pointer to first leftmost bit position within the "protocol ID" field;  pointer to third leftmost bit position within the "protocol ID" field;  pointer to fourth leftmost bit position within the "protocol ID" field;  pointer to fourth leftmost bit position within the "destination address" field)

Title: Implementing Access Control Lists Using A Balanced Hash Table of Access Control List Binary Comparison Trees

Example showing the construction of Balanced Hash Table of ACL. Binary Comparison Trees Example showing the creation of a Binary Comparison Tree for First In Sequence ACL. Rule in Rule Set

PERMIT PACKET
— Dest. Port = 28 <u>motch</u> —   <u>miss</u>   DEFAULT DENY
Protocol = TCP? match —— Dest. Addr. = 28.16.31.10 match —— DEFAULT DENY  DEFAULT DENY
PERMIT TCP ANY HOST 28.16.31.10 EQ 28

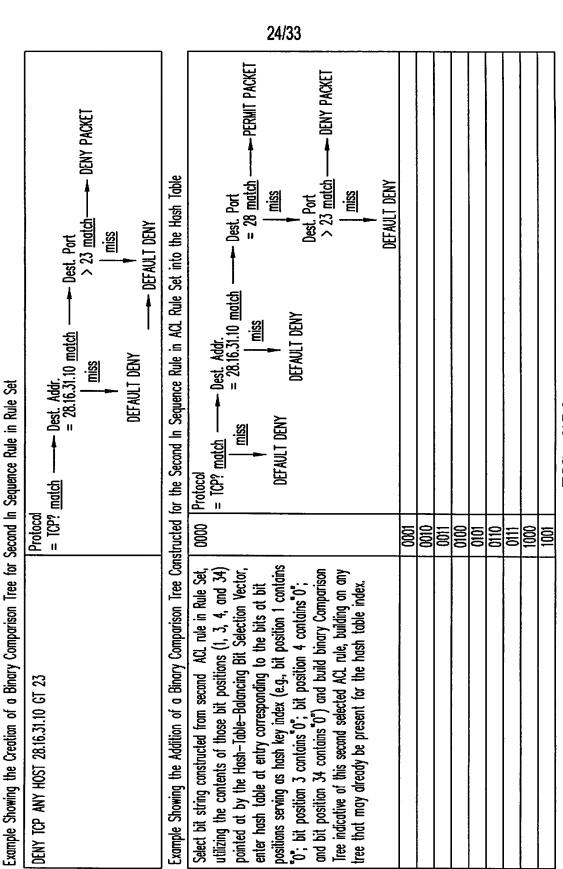
Example showing the addition of a Binary Comparison Tree constructed for the first in Sequence Rule in ACL Rule Set into the Hash Table

22/33										
Protocol = TCP? match — Dest. Addr. = 28.16.31.10 match — Dest. Port = 28.16.31.10 miss DEFAULT DENY DEFAULT DENY DEFAULT DENY DEFAULT DENY										
000	1000	8	9	0100	1010	0110	<u>=</u>	1000	<b>18</b>	1010
Select bit string constructed from first ACL rule in Rule Set, utilizing the contents of those bit positions (1, 3, 4, and 34) pointed at by the Hash-Table-Balancing Bit Selection Vector, enter hash table at entry corresponding to the bits at bit positions serving as hash key index (e.g., bit position 1 contains "0"; bit position 3 contains "0"; bit position 4 contains "0"; and bit position 34 contains "0"; and build binary Comparison Tree indicative of this first selected ACL rule										

FIG. 7D1

101 1100 1101 1110
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Title: Implementing Access Control Lists Using A Balanced Hash Table of Access Control List Binary Comparison Trees



Example Showing the Construction of Balanced Hash Table of ACL Binary Comparison Trees (cont.)

FIG. 703

1010	1011	1100	101	1110	1111	

Example Showing the Construction of Balanced Hash Table of ACL Binary Comparison Trees (cont.) Example Showing the Creation of a Binary Comparison Tree for Third In Sequence ACL Rule in Rule Set

DENY UDP ANY HOSI 30.22.21.5 EQ 11	F10(000)
	- Indian Adde
	= VUP: illatai —— Dest. Addi.
	DEFAULT DOUG
	DEFAULT DENY
	DEFAULT DENY

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26/33					
Protocol = TCP? match —— Dest. Addr. = 28.16.31.10 match —— Dest. Port = 28 match —— PERMIT PACKET   miss   miss   miss     DEFAULT DENY   Dest. Port   > 23 match —— DENY PACKET     miss   miss     miss   miss     miss   DEFAULT DENY     DEFAULT DENY PACKET     miss   DEFAULT DENY     miss   D					
0000	88	990	100	990	UIU

FIG 705

									27/33
									Protocol  = UDP? match —— Dest. Addr.  = 30.22.21.5 match —— Dest. Port  miss = 11 match —— DENY PACKET  DEFAULT DENY  DEFAULT DENY  DEFAULT DENY
0110	0111	1000	1001	1010	1011	1100	1101	1110	11
									Select bit string constructed from third ACL rule in Rule Set, utilizing the contents of those bit positions (1, 3, 4, and 34) pointed at by the Hash-Table-Balancing Bit Selection Vector, enter hash table at entry corresponding to the bits at bit positions serving as hash key index (e.g., bit position 1 contains 1"; bit position 3 contains 1"; bit position 34 contains 1"; and build binary Comparison Tree indicative of this third selected ACL rule

Title: Implementing Access Control Lists Using A Balanced Hash Table of Access Control List Binary Comparison Trees

-PERMIT PACKET - DENY PACKET Dest. Port > 23 match-► Dest. Port = 28 <u>match</u> -Example Showing the Addition of a Binary Comparison Tree Constructed for the Fourth In Sequence Rule in ACL Rule Set into the Hash Table <u>Miss</u> miss → PERMIT PACKETS Dest. Addr.
 28.16.31.10 match - Dest. Addr.
 30.22.21.X match DEFAULT DENY EIS: miss Example Showing the Creation of a Binary Comparison Tree for Fourth In Sequence ACL Rule in Rule Set Example Showing the Construction of Balanced Hash Table of ACL Binary Comparison Trees (cont.) DEFAULT DENY | 0000 | Protocol |= TCP? <u>motch</u> -Protocol = UDP? <u>match</u> -Miss Miss PERMIT UDP ANY HOST 30.22.21.X

FIG. 7D7

Application No.: 09/483,110
First Named Inventor: Faisal Haq
Title: Implementing Access Control Lists Using A Balanced

Hash Table of Access Control List Binary Comparison Trees

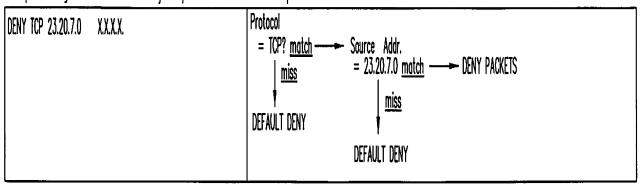
20/22

									29/33	
									Protocol  = UDP? motch —— Dest. Addr.  = 30.22.21.5 match —— Dest. Port  = 11 match —— Deny Packet    miss	
0110	<b>E</b>	99	<u>5</u>	1010	101	1100	1101	1110	#	
									Select bit string constructed from fourth ACL rule in Rule Set, utilizing the contents of those bit positions (1, 3, 4, and 34) pointed at by the Hash-Table-Balancing Bit Selection Vector, enter hash table at entry corresponding to the bits at bit positions serving as hash key index (e.g., bit position 1 contains "1"; bit position 3 contains "1"; bit position 4 contains "1"; and bit position 34 contains "1") and build binary Comparison Tree indicative of this fourth selected ACL rule, building on any tree that may already be present for the hash table index	

30/33

Example Showing the Construction of Balanced Hash Table of ACL Binary Comparison Trees (cont.)

Example Showing the Creation of a Binary Comparison Tree for Fifth In Sequence ACL Rule in Rule Set



Example Showing the Addition of a Binary Comparison Tree Constructed for the Fifth In Sequence Rule in ACL Rule Set into the Hash Table

Select bit string constructed from fifth ACL rule in Rule Set, utilizing the contents of those bit positions (1, 3, 4, and 34) pointed at by the Hash-Table-Balancing Bit Selection Vector, enter hash table at entry corresponding to the bits at bit positions serving as hash key index (e.g., bit position 1 contains "0"; bit position 3 contains "0"; bit position 4 contains "0"; and bit position 34 contains "X") and build binary Comparison Tree indicative of this fifth selected ACL rule, building on any tree that may already be present for the hash table index; however, since bit at bit position 34 is X, the rule will be appended at both 0000 and 0001, since bit position 34 may be either 0 or 1. In addition, since the rule itself applies to any destination address, the miss branch of all destination branches present must feed back into the source address compare instruction associated with this Fifth Rule.

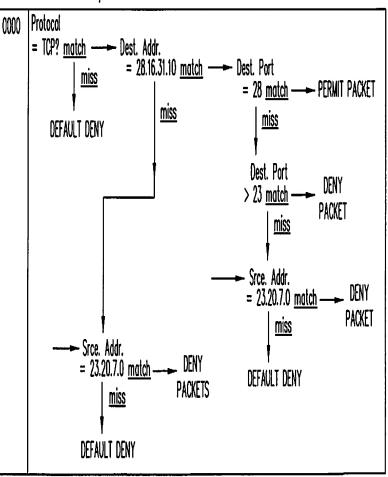


FIG. 7D9

	31/33
Protocol = TCP? match — Source Addr. = 23.20.7.0 match — DENY PACKETS   miss   miss   miss     DEFAULT DENY     DEFAULT DENY	Protocol = UDP? match —— Dest. Addr. = 30.22.21.5 match —— Dest. Part   miss = 11 match —— DENY PACKET   miss = 30.22.21.X match —— PERMIT PACKET   miss   miss
	010101111111111111111111111111111111111
Select bit string constructed from fifth ACL rule in Rule Set, utilizing the contents of those bit positions (1, 3, 4, and 34) pointed at by the Hash-Table-Balancing Bit Selection Vector, enter hash table at entry corresponding to the bits at bit positions serving as hash key index (e.g., bit position 1 contains "0"; bit position 3 contains "0"; bit position 3 contains "0"; bit position 34 contains "X") and build binary Comparison Free indicative of this fifth selected ACL rule, building on any tree that may already be present for the hash table index; however, since bit at bit position 34 is X, the rule will be appended at both 0000 and 0001, since bit position 34 may be either 0 or 1.	

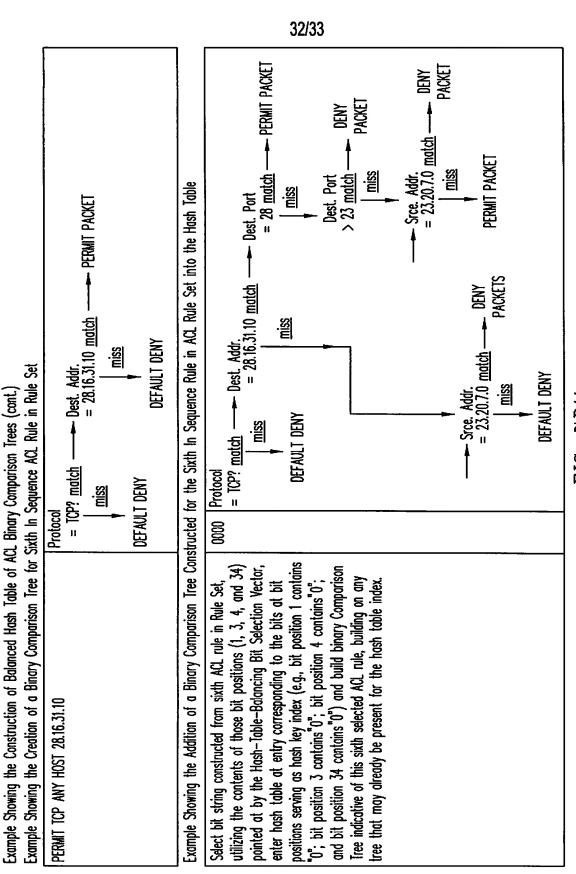


FIG. 7D11

Title: Implementing Access Control Lists Using A Balanced Hash Table of Access Control List Binary Comparison Trees

